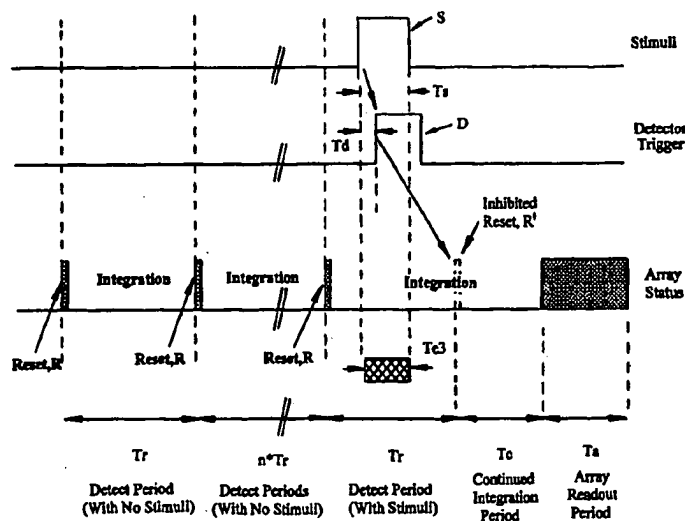




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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(21) International Application Number: PCT/GB99/01365 (22) International Filing Date: 30 April 1999 (30.04.99) (30) Priority Data: 9809482.4 1 May 1998 (01.05.98) GB (71) Applicant (for all designated States except US): VLSI VISION LIMITED [GB/GB]; Aviation House, 31 Pinkhill, Edinburgh EH12 7BF (GB). (72) Inventors; and (75) Inventors/Applicants (for US only): HURWITZ, Jonathan, Ephraim, David [GB/GB]; 17 (3F1) Gladstone Terrace, Edinburgh EH9 1LS (GB). DENYER, Peter, Brian [GB/GB]; 5 Albert Terrace, Edinburgh EH10 5EA (GB). (74) Agents: McCALLUM, William, Potter et al.; Cruikshank & Fairweather, 19 Royal Exchange Square, Glasgow G1 3AE (GB).	(81) Designated States: JP, US, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published With international search report.	

(54) Title: IMAGE CAPTURE CONTROL



(57) Abstract

A method of operating a solid state image sensor (1) for the acquisition of an image generated by an asynchronous stimulus (S) is described in which the sensor is operated in conjunction with at least one detector (4) which detects the said asynchronous stimulus. The sensor is regularly reset so as to commence integration from a reset state of the sensor each time a period T_r has elapsed. The output of the detector(s) prior to each reset (R) is used to determine whether that reset is inhibited or not, whereby the likelihood of the stimulus being corrupted is prevented, or at least substantially reduced. A method is also proposed in which a portion of the sensor array is itself used as the detector (4) for detecting the asynchronous stimulus. A solid state image sensor incorporating a reset inhibition control function for carrying out the described method is also claimed.

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IMAGE CAPTURE CONTROL

This patent application relates to techniques for acquiring images from a solid-state imager when exposure to the scene is
5 controlled by either an asynchronous lighting strobe, or by the asynchronous opening of a shutter. The techniques that we describe do not require an electronic connection between the strobe/shutter and the sensor in order to work, and are hence applicable for use in systems where there is a physical
10 reason, or an electronic reason, why this connection is not feasible.

Solid state image sensors dominate electronic imaging applications such as CCTV, video cameras and camcorders, and
15 scanners, and are the basis of newly developed markets such as PC-cameras for videoconferencing, medical vision, machine vision and Digital Stills Cameras.

One popular form of image sensor is the Charge Coupled Device
20 (CCD), whilst sensors built entirely within standard CMOS processes are also gaining currency. Both have their relative merits when applied to these techniques.

As used herein the expression "asynchronous stimulus" means a
25 stimulus the timing of whose occurrence is not known in advance and which stimulus is associated with the presentation of an image to be captured to the solid state image sensor. As discussed herein various kinds of solid state image sensors known in the art may be used in the present invention,
30 including CCD sensors as well as sensors such as those disclosed in our earlier patent publication WO91/04633, in which, following a resetting of the sensing cells, charge is built up on the sensing cells in response to incident

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radiation impinging thereon and the built up charge subsequently converted into a voltage signal during an integration period, and this cycle repeated upon the next resetting of the sensing cells.

5

In some systems it is desirable to separate the operation of the sensor from the exposure mechanism. One such application is Electronic Film, for use in conventional Silver Halide Cameras such as 35mm SLR (Single Lens Reflex). Here the solid
10 state sensor replaces the chemical film within the camera, and as with chemical film the exposure is controlled by the shutter of the camera. In order that such an Electronic Film can work without user modification of the camera to access the shutter control signal, or with older non-electronic cameras,
15 it is necessary for the sensor to auto-detect that it has been exposed. This system must offer a high probability of successful detection, and be scene independent, working under the widest possible range of camera exposures, and additional operating conditions such as flash and fill-in flash.

20

Another application is in medical vision and in machine vision, where exposure/illumination occurs through an illumination strobe, and there are physical or electronic reasons why a synchronisation pulse between the light source
25 and the sensor cannot occur. For example it may be necessary to isolate the light source from the detector for reasons of safety, as in an X-ray system.

Fig. 1 shows a conventional general imaging system
30 incorporating a solid state image sensor 1 (incorporating an array of sensing cells) with a shutter 2 (electronic, mechanical or electromechanical), a lighting strobe 3, and a detector 4. The imaging system also includes strobe/shutter

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control means 5, and sensor timing and detector control means 6. There is no timing interaction between the strobe/shutter control means 5, and the sensor timing and detector control means 6.

5

The shutter 2 and/or the lighting strobe 3 provide means of asynchronous stimulation of the image sensor 1 in order to capture an image of an object 7. The classic approach to the problem would be try to detect the asynchronous event and to

10 then subsequently instigate an exposure and acquisition sequence for the image sensor. The problem with this approach is that it puts design pressure on achieving an asynchronous event detector that is sufficiently fast and reliable that the interaction between activating the image sensor and the

15 asynchronous stimulus does not corrupt the effective exposure.

Fig. 2 shows a timing diagram of an image acquisition sequence commonly used with the imaging system of Fig.1, where the detector triggers the release from reset of the array of the sensor 1, putting it into integration. The array is then read

20 when the stimulus has gone away. In this example the solid state image sensor 1 and the detector 4 see the stimulus simultaneously, as in the case of a lighting strobe 3. As can be seen the time for the detector to trigger, T_d , reduces the effective amount of the stimulus, T_s , to an amount T_e , that is

25 approximately equal to:-

$$T_e = T_s - T_d$$

If there is a spatial distance between the detector 4 and the image plane of the solid state sensor 1 with respect to the

30 stimulus, as in the case of a blade shutter 2 in an SLR camera, then the detector trigger time T_d can result in a gradient of exposure across the array of the sensor 1. Fig. 2b shows an example of what would happen to an array if the

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detector 4 was located to the left hand side of the array, and the shutter 2 was opening from the right hand side of the array. If Tsh1 is the time the shutter takes to cross the array and Tsh2 is the subsequent time for the shutter to pass from the array to the detector, then as the diagram shows the two sides of the array see different effective stimuli, Te1 and Te2, as defined by:-

$$Te1 = Ts - Td - Tsh1 - Tsh2$$

$$Te2 = Ts - Td - Tsh2$$

10

This problem can be reduced by using detectors on the side of the shutter that opens first, but still if the time to detect Td is greater than the time to reach the array Tsh2, then there will be a gradient of exposures across the array. The effective stimuli will be somewhere between the following values:-

$$(Ts - Tsh) < Te < (Ts - Td), \text{ where } Tsh2 > Td$$

dependant on the position in the array. This is clearly undesirable.

20

We describe a more radical approach to the problem that greatly increases the probability of successful detection of the asynchronous event with no degradation of the stimulus.

25 Thus, according to one aspect of the present invention we provide a method of operating a solid state image sensor for the acquisition of an image generated by an asynchronous stimulus, wherein said image sensor is operated in conjunction with at least one detector which, directly or indirectly, 30 detects the said asynchronous stimulus, said image sensor is regularly reset so as to commence integration from a reset state of the sensor each time a predetermined period has elapsed, and an output from said at least one detector prior

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to each reset is used to determine whether that reset is inhibited or not.

According to a further aspect of the invention we provide a method of using a solid state image sensor, comprising an array of sensing cells, for the acquisition of an image generated by an asynchronous stimulus, wherein said image sensor is regularly reset so as to commence integrating from a reset state of the sensor each time a predetermined period has elapsed, and wherein a portion of the array of the sensor is read prior to each said reset and the value of this read is used to determine whether the subsequent reset should be inhibited or not.

According to yet another aspect of the present invention we provide image capture control apparatus suitable for use with a solid state image sensor for the acquisition of an image presented to the sensor in response to an asynchronous stimulus, said apparatus comprising at least one detector means formed and arranged for detecting, in use of the apparatus, directly or indirectly, a said asynchronous stimulus, and reset inhibition control signal output means formed and arranged for generating a reset inhibition control signal in response to detection of said asynchronous stimulus and supplying it, directly or indirectly, in use of the apparatus, to a reset signal generating means operatively coupled to said solid state image sensor, so as to inhibit the application of at least one subsequent reset signal to the sensor.

30

The image capture control apparatus may be provided in a single device. Alternatively, the apparatus may be provided in the form of separate modules.

In a further aspect, the present invention provides a camera having a solid state image sensor, wherein is provided image capture control apparatus as above-described.

5

Preferred embodiments of the invention will now be described by way of example only and with reference to the accompanying drawings in which:

Fig.1 is a schematic diagram of an imaging system

10 incorporating a solid state sensor exposed to an asynchronous stimulus;

Fig.2a is a diagram showing the relative timing of various operations carried out in a conventional method of operating the system of Fig.1;

15 Fig.2b is a diagram showing the relative timing of various operations carried out in a further known prior art method of operating the system of Fig.1;

Fig.3a is a diagram of the relative timing of various operations carried out in a method of operating the system of

20 Fig.1 according to the present invention, where an asynchronous event occurs in a "detect period" T_r ;

Fig.3b is a diagram of the relative timing of various operations carried out in a method of operating the system of Fig.1 according to the present invention, where the

25 asynchronous event straddles two detect periods T_r ;

Fig.3c is a diagram of the relative timing of various operations carried out in a method of operating the system of Fig.1 according to the present invention, where the asynchronous event occurs close to the end of the detect

30 period T_r ;

Fig.4 is a diagram of the relative timing of various operations carried out in another method of operating the system of Fig.1 according to the present invention, in which a

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portion of the sensor array is used to detect the asynchronous stimulus; and

Fig.5 is a block diagram of one embodiment of image capture control apparatus for carrying out the method of the invention.

The method of the invention can be briefly described in the following manner, with reference to the imaging system of Fig.1:-

- 10 a) The image sensor is regularly reset, at a repetition rate of $1/T_r$. The period T_r between each reset pulse R is hereinafter referred to as the detect period T_r . In this period T_r between the reset pulses R the image sensor 1 is integrating any incident light.
- 15 b) If during a given detect period T_r the detector 4 has fired, indicating that there has been some asynchronous stimulus, which is of duration T_s , then the next reset pulse R' is inhibited, and the sensor enters its "continued integration period", T_c .
- 20 c) In the continued integration period, T_c , the integration of the array is continued to beyond the extent of the longest asynchronous stimulation, $T_s(\max)$. This may either be a fixed time or a time based on a trigger by a detector that the stimulus has gone away. The sensor now
25 enters the readout period.
- d) In the array readout period, T_a , the array is readout, and can then go back to the detect period T_r to await the next asynchronous event (i.e. stimulus).

30 We have called this approach the 'inhibited reset' approach.

Fig 3a shows the basic timing for an asynchronous event S that occurs totally inside the detect period T_r , and Fig 3b an

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event S that would straddle a reset pulse R (hereinafter referred to as a "reset" R), but for the 'inhibited reset' (i.e. the event S straddles the inhibited reset R'). The time for the detector to fire is T_d , and the probability of acquiring the asynchronous event without any corruption is :-

Probability of success = $(T_r - T_d)/T_r$ and normally $T_r \gg T_d$

Note in both cases the effective exposure, T_{e3} , seen by all of the array and the detector is the full time of the stimuli,

$$T_{e3} = T_s$$

Fig 3c shows the case where the stimulus occurs $< T_d$ away from where the reset R would occur. In this case the reset R would NOT be inhibited in time, but it is important to note that this is no worse than the classic approach described earlier with reference to Fig.2c.

In this case the effective exposure, T_{e4} is :-

$$T_{e4} = T_s - T_d$$

Fig.5 illustrates schematically one embodiment of image capture control apparatus for implementing the above-described method of the invention. The apparatus 10 comprises the detector 4, a reset signal generating unit 11 for generating the reset signals R, and reset inhibition control signal output means 12 for generating a reset inhibition control signal for supplying to the reset signal generating unit 11. When the detector 4 detects an asynchronous stimulus it outputs a detection signal D (see Fig.3) to the reset inhibition control signal output means 12 which generates a reset inhibition control signal C_r which is supplied to the

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reset signal generating unit 11 so as to inhibit the application of the subsequent reset pulse R' to the sensor array. It will be appreciated that the reset inhibition control signal output means 12, if desired, may be
5 incorporated in the detector 4 (e.g. in a single device), or may be incorporated in the reset signal generating unit 11, or may be provided as a separate module. Similarly, the reset signal generating unit 11 may be incorporated in the sensor 1. Equally, the apparatus 10 may together be incorporated in the
10 image sensor 1, if desired, or may be provided as one or more separate modules for use therewith.

The detector 4 in both the classic approach and the 'inhibited reset' approach need not be a direct detector, i.e. another
15 optical sensor that is also looking for the same type of optical stimuli, although this is by far the most popular approach. An example of an indirect detector which could be used is a vibration transducer, or a sound transducer, for detecting the movement of a physical shutter.

20

In the case of the optical detector 4, it is often co-located with the image sensor 1, but it is not possible to put it in the same focal plane as the image sensor. This can give problems in a lensed system as the detector may not be focused
25 on a part of the scene with sufficient luminance to trigger it.

However, with our 'Inhibited reset approach' it is possible to use a sub-sampled portion of the sensor array, in such a way
30 that the sensor array itself can act as the detector of the asynchronous stimulus. This is because the array is already integrating during its detect period, therefore by reading it before the decision to inhibit reset or not, we have a sample

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of the light that has been integrated by the array. By comparing these values with the values obtained when there has been no stimulus, we have a measure of the change. If this change is greater than a user defined threshold, then we say that an asynchronous stimulus has occurred. The choice of this threshold, relative to the lowest energy stimulus it is desired to detect, determines the effective time to detect, T_d , in the following way. With reference to Fig.4, if T_s is the longest stimuli to cause saturation of the image sensor, T_{sa} is the time to read the sub-array, and P_t is the percentage of saturation that is required to trigger a threshold of detection, then the effective T_d is

$$T_d = (T_s * P_t) + T_{sa}$$

15

The major advantage of this approach is that the array itself is acting as the detector and is therefore in the focal plane of the focused image. Spatially distributing the sub-sample, greatly increases the probability that some of the pixels of the sub-sample are in areas with sufficient luminance.

20

It is of course possible to use our described 'Inhibited reset approach' with a plurality of detectors and sub-sampled arrays, to determine if an asynchronous event has occurred.

25

Various other modifications and variations of the above-described embodiments are also possible without departing from the scope of the invention. For example, in relation to the continued integration period, T_c , it will be appreciated that this could be an extended period corresponding in effect to the inhibition of more than one reset pulse i.e. a series of successive reset pulses.

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CLAIMS

1. A method of operating a solid state image sensor (1) for the acquisition of an image generated by an asynchronous stimulus (S), wherein said image sensor is operated in conjunction with at least one detector (4) which, directly or indirectly, detects the said asynchronous stimulus, said image sensor is regularly reset so as to commence integration from a reset state of the sensor each time a predetermined period (Tr) has elapsed, and an output from said at least one detector prior to each reset (R) is used to determine whether that reset is inhibited or not.

2. A method according to claim 1 wherein the detector outputs a detection signal (D) when said asynchronous stimulus (S) is detected, and said detection signal (D) is used to trigger a reset inhibition control signal (Cr) for inhibiting the subsequent reset signal (R').

3. A method of using a solid state image sensor (1), comprising an array of sensing cells, for the acquisition of an image generated by an asynchronous stimulus (S), wherein said image sensor is regularly reset so as to commence integrating from a reset state of the sensor each time a predetermined period (Tr) has elapsed, and wherein a portion of the array of the sensor (1) is read prior to each said reset (R) and the value of this read is used to determine whether the subsequent reset (R') should be inhibited or not.

4. A method according to claim 3, wherein said portion of the array read prior to each reset (R) comprises a plurality of sensing cells which are spatially distributed throughout the array of sensing cells.

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5. Image capture control apparatus suitable for use with a solid state image sensor (1) for the acquisition of an image presented to the sensor in response to an asynchronous stimulus (S), said apparatus comprising at least one detector means (4) formed and arranged for detecting, in use of the apparatus, directly or indirectly, a said asynchronous stimulus (S), and reset inhibition control signal output means (12) formed and arranged for generating a reset inhibition control signal in response to detection of said asynchronous stimulus (S) and supplying it, directly or indirectly, in use of the apparatus, to a reset signal generating means (11) operatively coupled to said solid state image sensor, so as to inhibit the application of at least one subsequent reset signal (R') to the sensor.

6. Image capture control apparatus according to claim 5, wherein said at least one detector means (4) and said reset inhibition control signal output means (12) are provided in a single device.

7. Image capture control apparatus according to claim 5 or claim 6, wherein said reset inhibition control signal output means (12) and said reset signal generating means (11) are provided together in a single device.

8. A camera having a solid state image sensor, wherein is provided image capture control apparatus according to claim 5, claim 6 or claim 7.

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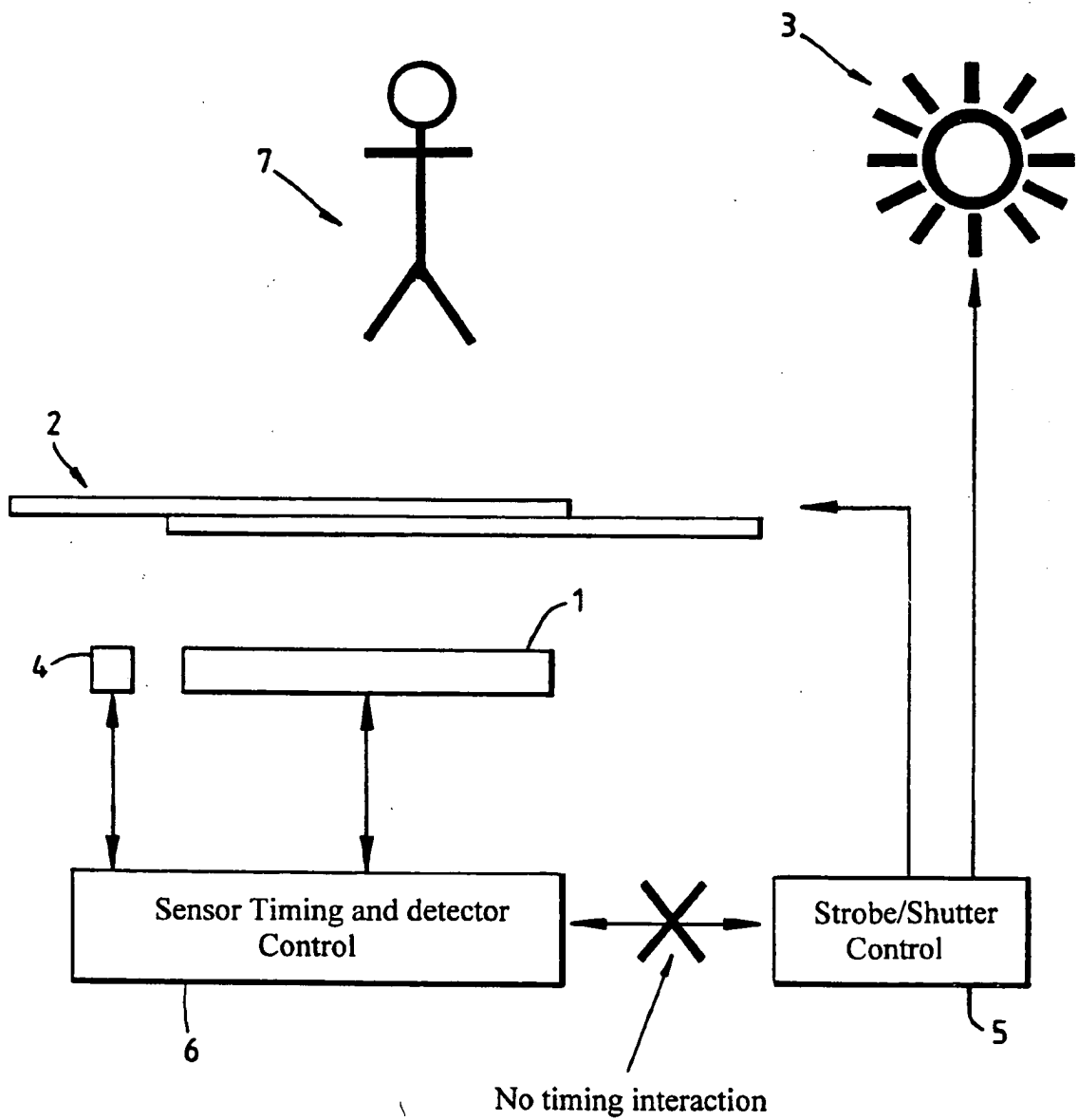


Fig. 1

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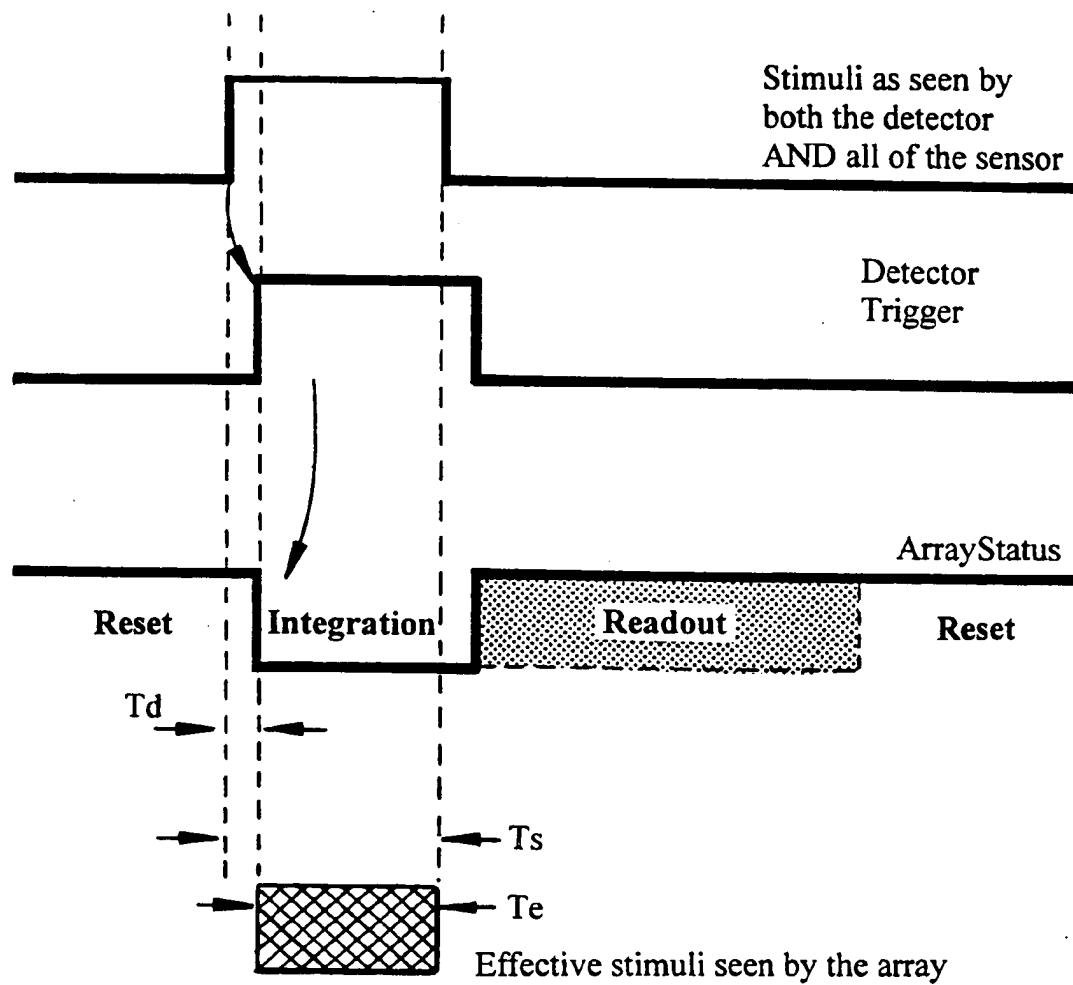


Fig. 2a

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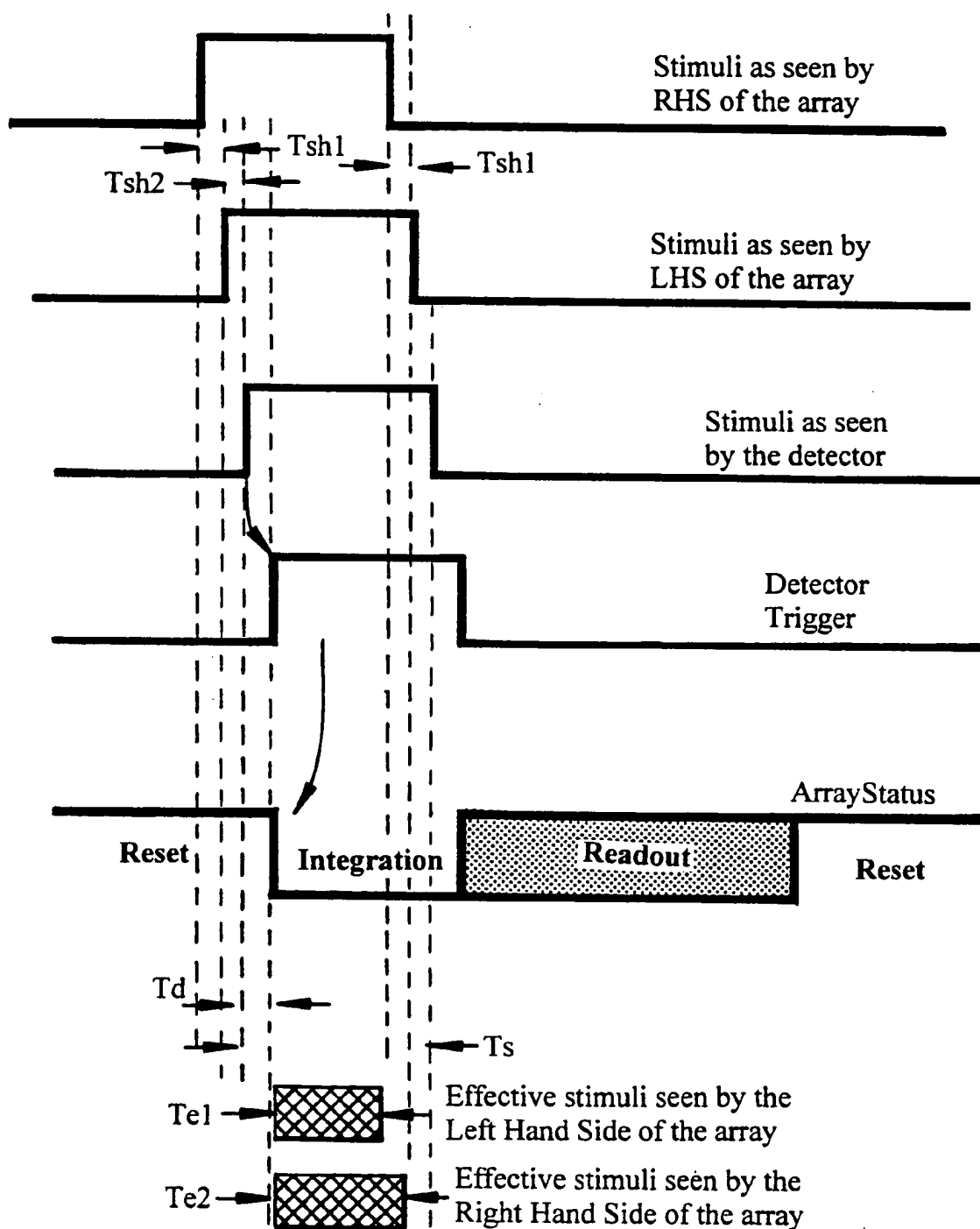
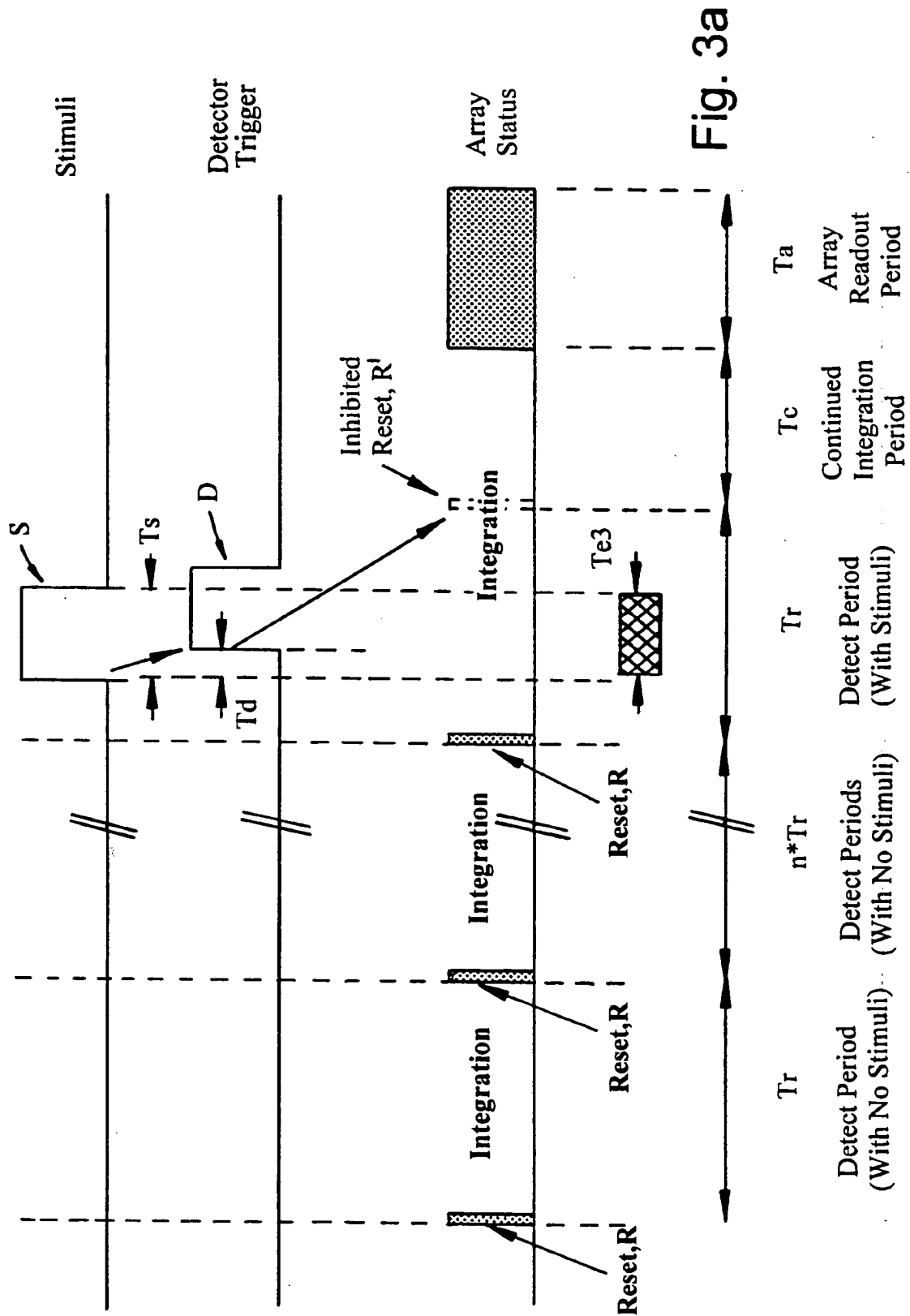
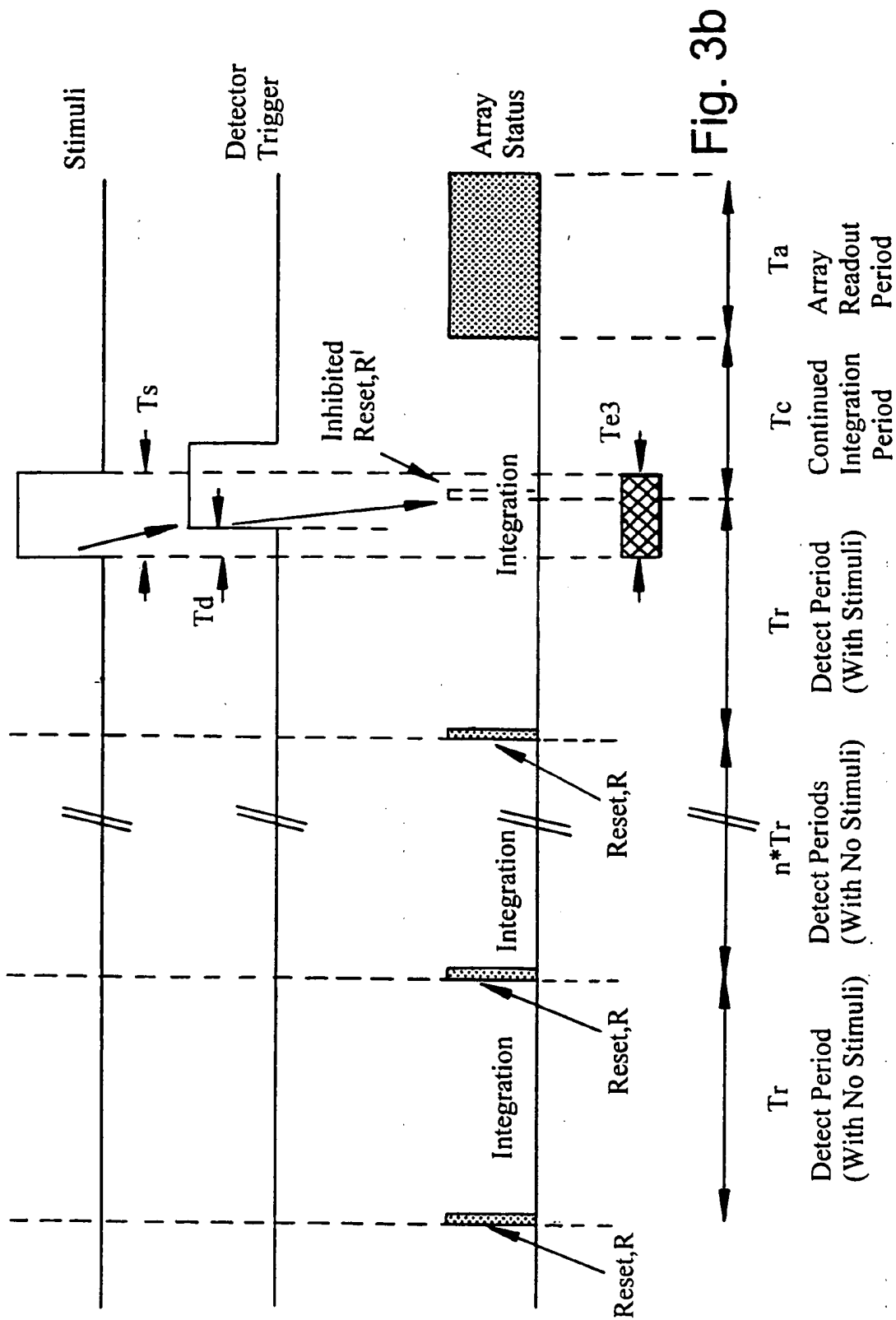


Fig. 2b





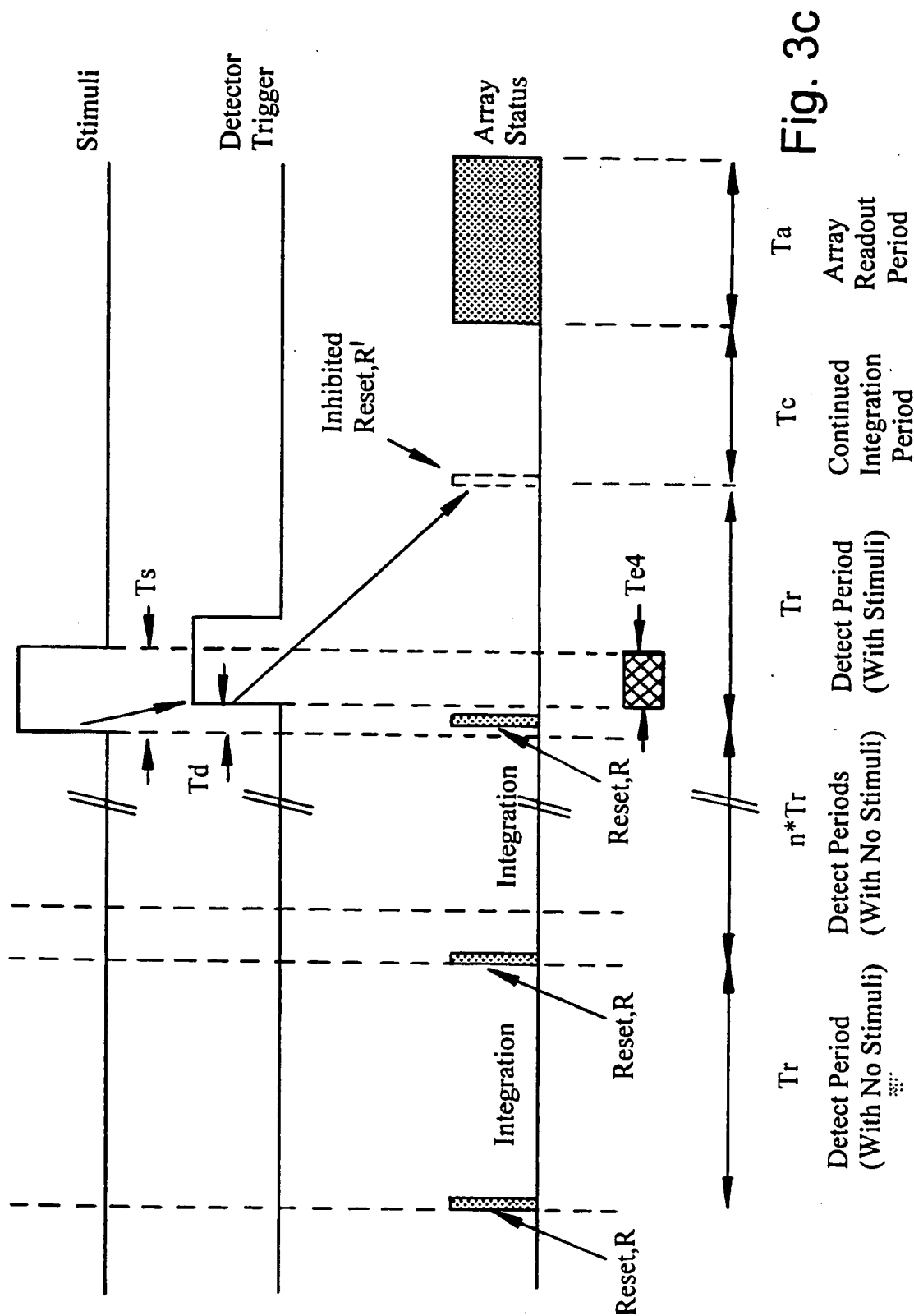


Fig. 3c

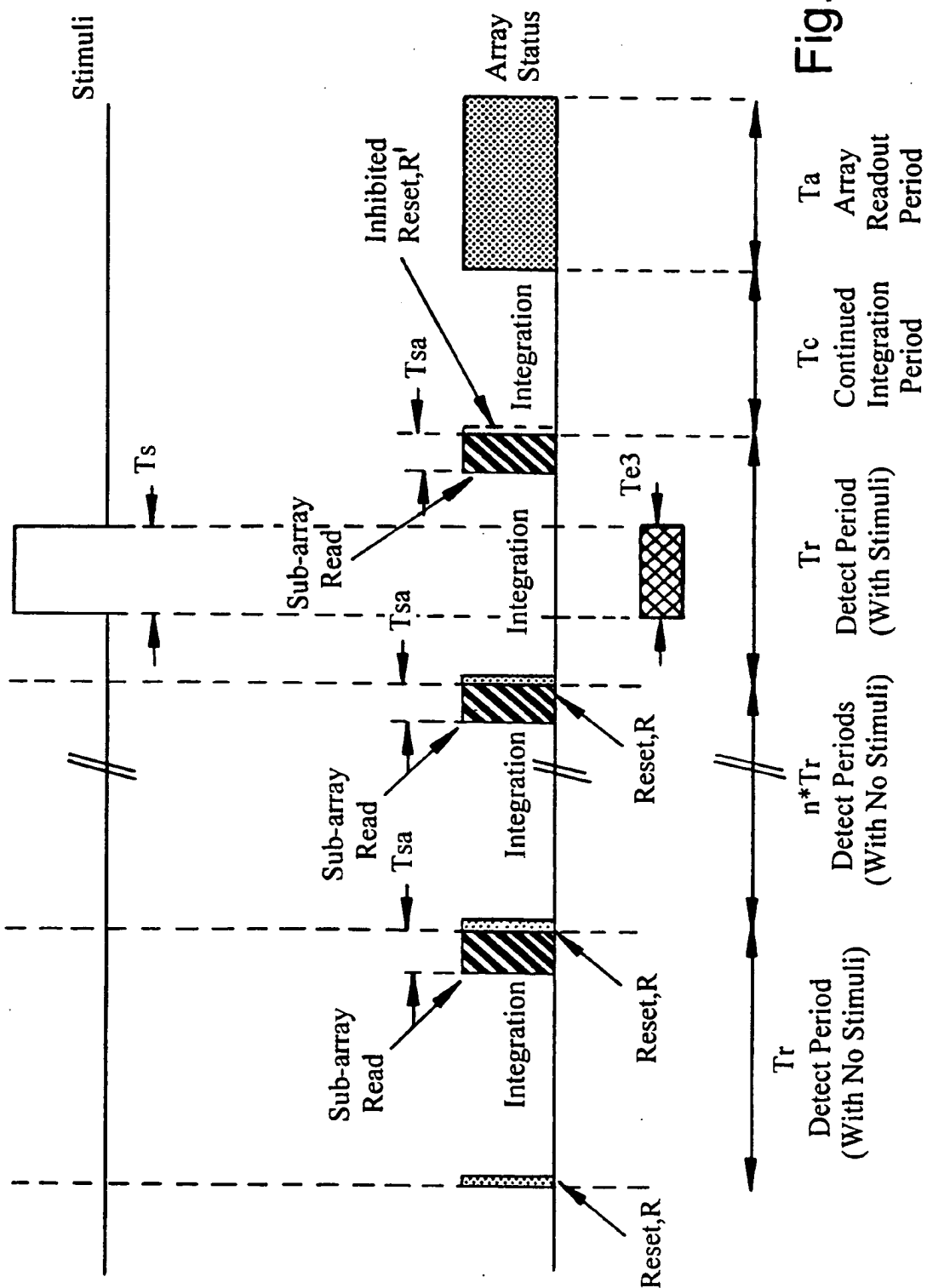


Fig. 4

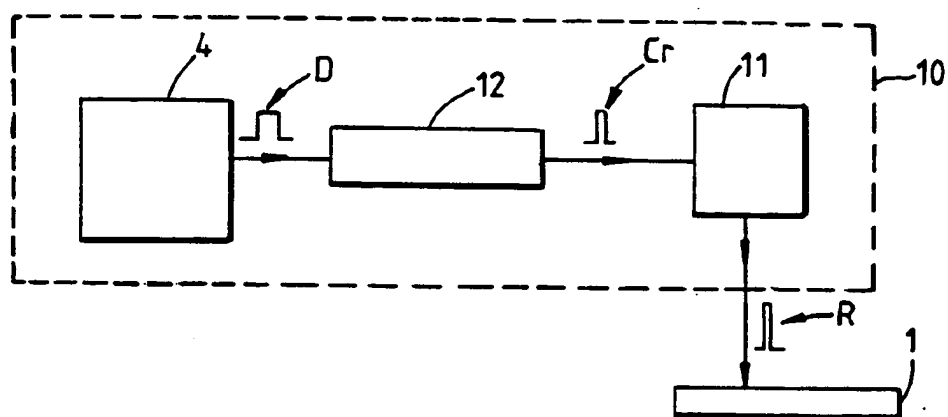


Fig. 5

INTERNATIONAL SEARCH REPORT

national Application No
PCT/GB 99/01365

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 H04N3/15

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 6 H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X A	US 5 422 670 A (FUKUI HIROSHI) 6 June 1995 (1995-06-06) column 1, line 49 - column 2, line 42; figures 2,3C -----	1,2,5-8 3

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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INTERNATIONAL SEARCH REPORT

Information on patent family members

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5422670 A	06-06-1995	JP 6125502 A	06-05-1994